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Inventor Name Search Result

Your Search was:

Last Name = MISHRA

First Name = SHRIDHAR

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------------------|-------------------------|--------|------------|---------------------------------------------------------------------------------------------------|--------------------------------|
| 09964204 | Not Issued | 161 | 09/26/2001 | Testing of SOC | MISHRA, SHRIDHAR MUBARQA |
| 10065005 | 7080184 | 150 | 09/09/2002 | ISDN-BASED BUS INTERFACE | MISHRA, SHRIDHAR MUBARQA |
| 10526529 | Not Issued | 30 | 03/02/2005 | Data switch and a method for broadcast packet queue estimation | MISHRA, SHRIDHAR MUBARQA |
| 10526700 | Not Issued | 30 | 03/04/2005 | Method and system for determining conformance of a data key with rules by means of memory lookups | MISHRA, SHRIDHAR MUBARQA |
| 10526811 | Not Issued | 30 | 03/04/2005 | Intelligent stacked switching system | MISHRA, SHRIDHAR MUBARQA |
| 10526886 | Not Issued | 41 | 03/04/2005 | Method and system for controlling bandwidth allocation | MISHRA, SHRIDHAR MUBARQA |
| 10526992 | Not Issued | 30 | 03/07/2005 | Parser for parsing data packets | MISHRA, SHRIDHAR MUBARQA |
| 10526990 | Not Issued | 30 | 03/07/2005 | Configurable fast ethernet and gigabit ethernet data port | MISHRA, SHRIDHAR MUBARQA |

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Inventor Name Search Result

Your Search was:

Last Name = ARDHANARI

First Name = GURUPRASAD

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------------------|------------|--------|------------|---------------------------------------------------------------------------------------------------|-----------------------|
| 10526529 | Not Issued | 30 | 03/02/2005 | Data switch and a method for broadcast packet queue estimation | ARDHANARI, GURUPRASAD |
| 10526700 | Not Issued | 30 | 03/04/2005 | Method and system for determining conformance of a data key with rules by means of memory lookups | ARDHANARI, GURUPRASAD |
| 10526886 | Not Issued | 41 | 03/04/2005 | Method and system for controlling bandwidth allocation | ARDHANARI, GURUPRASAD |
| 11364740 | Not Issued | 25 | 02/28/2006 | Data switch, and communication system using the data switch | ARDHANARI, GURUPRASAD |
| 11674501 | Not Issued | 20 | 02/13/2007 | METHOD OF CONTROLLING A DATA FLOW, TRANSMITTER AND DATA TRANSMISSION SYSTEM | ARDHANARI, GURUPRASAD |

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